Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L33	39	(cpu and dma) with conflict$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/20 09:41
L41	213	(cpu and dma) same (wait$3 adj state)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/20 10:26
L47	173	41 and (@ad<"20000919")	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/20 12:43
L50	4098	((halt$3 inhibit$3 delay$3 stall$3) with cpu) same (dma wirh (access read$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/20 11:03
L51	169	((halt$3 inhibit$3 delay$3 stall$3) with cpu) same (dma with (access read$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/20 11:47
L55	95	(cpu with dma) with (access$3 read$3) with (simultaneous$2 concurrent$2 conflict$3 collision)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/20 12:43
L56	62	55 and (@ad<"20000919")	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/20 12:43
S1	956	710/22	US-PGPUB; USPAT	OR	ON	2004/11/15 10:37
S2	39	710/22 and lcd	US-PGPUB; USPAT	OR	ON	2003/01/22 13:54
S7	1014	710/22-28.ccls.	US-PGPUB; USPAT	OR	ON	2003/01/22 15:20
S10	2013	dma and ((first and second) near (buffer or memory))	US-PGPUB; USPAT	OR	ON	2003/01/22 17:15
S18	4441	dma and ((first and second) near3 (memory or buffer))	US-PGPUB; USPAT	OR	ON	2003/01/24 12:43
S19	672	(dma and ((first and second) near3 (memory or buffer))) and (delay with (cpu or microprocessor or processor))	US-PGPUB; USPAT	OR	ON	2003/01/24 13:06
S20	15	((dma and ((first and second) near3 (memory or buffer))) and (delay with (cpu or microprocessor or processor))) and gps	US-PGPUB; USPAT	OR	ON	2003/01/24 13:03
S21	271	((dma and ((first and second) near3 (memory or buffer))) and (delay with (cpu or microprocessor or processor))) and (select$3) and (isolat$3)	US-PGPUB; USPAT	OR	ON	2003/01/24 13:08
S22	1015	710/22-28.ccls.	US-PGPUB; USPAT	OR	ON	2003/01/24 13:09
S23	796	710/22-28.ccls. and dma	US-PGPUB; USPAT	OR	ON	2003/01/24 13:09
S24	726	(710/22-28.ccls. and dma) and (first and second) and (memory or buffer)	US-PGPUB; USPAT	OR	ON	2003/01/24 13:42
S25	352	((710/22-28.ccls. and dma) and (first and second) and (memory or buffer)) and (delay$3 or suspend$4)	US-PGPUB; USPAT	OR	ON	2003/01/24 13:10
S27	356	(710/22-28.ccls. and dma) and ((first and second) near3 (memory or buffer))	US-PGPUB; USPAT	OR	ON	2003/01/24 13:49
S28	243	((710/22-28.ccls. and dma) and ((first and second) near3 (memory or buffer))) and cpu	US-PGPUB; USPAT	OR	ON	2003/01/24 13:49

S29	49	(((710/22-28.ccls. and dma) and ((first and second) near3 (memory or buffer))) and cpu) and ((hold or suspend or delay) with cpu)	US-PGPUB; USPAT	OR	ON	2003/01/24 14:15
S31	10	casey near david	US-PGPUB; USPAT	OR	ON	2003/01/24 14:18
S32	76	345/538	US-PGPUB; USPAT	OR	ON	2004/11/17 14:13
S33	17	345/538 and dma	US-PGPUB; USPAT	OR	ON	2003/01/24 14:48
S36	95	709/212.ccls. and dma	US-PGPUB; USPAT	OR	ON	2003/01/24 14:48
S37	55	(709/212.ccls. and dma) and cpu	US-PGPUB; USPAT	OR	ON	2003/01/24 14:48
S40	200	710/25	US-PGPUB; USPAT	OR	ON	2003/01/24 15:05
S41	102	710/25 and dma	US-PGPUB; USPAT	OR	ON	2003/01/24 15:05
S42	99	(710/25 and dma) and (cpu or microprocessor or controller)	US-PGPUB; USPAT	OR	ON	2003/01/24 15:05
S43	13	((710/25 and dma) and (cpu or microprocessor or controller)) and ((delay or hold or suspend or stop) with cpu)	US-PGPUB; USPAT	OR	ON	2003/01/24 15:28
S44	45	((710/25 and dma) and (cpu or microprocessor or controller)) and ((delay or hold or suspend or stop) with transfer$4)	US-PGPUB; USPAT	OR	ON	2003/01/24 15:07
S45	5	("4706221" | "5175864" | "5404335" | "5530901" | "5561816").PN.	USPAT	OR	ON	2003/01/24 15:21
S47	88	DMA AND GPS AND ANTENNA	US-PGPUB; USPAT	OR	ON	2003/01/24 15:31
S48	8	(DMA near transfer$4) same (bypass$4 with cpu)	US-PGPUB; USPAT	OR	ON	2003/01/24 15:32
S52	353	dma and gps	US-PGPUB; USPAT	OR	ON	2003/01/24 17:38
S53	88	(dma and gps) and antenna	US-PGPUB; USPAT	OR	ON	2003/01/24 17:38
S54	109	dma and (gps near receiver)	US-PGPUB; USPAT	OR	ON	2003/01/24 17:38
S55	450	ram same (block) same partition$3	US-PGPUB; USPAT	OR	ON	2003/01/27 09:31
S56	369	(ram same (block) same partition$3) and (first and second) and portion	US-PGPUB; USPAT	OR	ON	2003/01/27 09:31
S57	9	ram same (block) same partition$3 same ((first and second) and portion)	US-PGPUB; USPAT	OR	ON	2003/01/27 09:37
S58	643	711/104	US-PGPUB; USPAT	OR	ON	2003/01/27 09:38
S59	34	711/104 and ram and block and partition	US-PGPUB; USPAT	OR	ON	2003/01/27 09:38
S62	0	suppress near cpu near clock	US-PGPUB; USPAT	OR	ON	2003/06/17 13:36
S63	1	suppressing near cpu near clock	US-PGPUB; USPAT	OR	ON	2003/06/17 13:45
S64	59	((suppress$3 or stall$3 or hold$3) near3 cpu near2 clock)	US-PGPUB; USPAT	OR	ON	2003/06/17 13:51
S65	35	(((suppress$3 or stall$3 or hold$3) near3 cpu near2 clock)) and dma	US-PGPUB; USPAT	OR	ON	2003/06/17 13:46
S66	107	((suppress$3 or stall$3 or hold$3 or halt$3) near3 cpu near2 clock)	US-PGPUB; USPAT	OR	ON	2003/06/17 13:51
S67	43	(((suppress$3 or stall$3 or hold$3 or halt$3) near3 cpu near2 clock)) and dma	US-PGPUB; USPAT	OR	ON	2003/06/24 17:20

S68	1684	710/100	US-PGPUB; USPAT	OR	ON	2003/06/24 17:21
S69	55	710/100 and ((suspend$3 or halt$4 or suppress$4 or stop$4) near2 clock)	US-PGPUB; USPAT	OR	ON	2003/06/24 17:24
S70	22	(710/100 and ((suspend$3 or halt$4 or suppress$4 or stop$4) near2 clock)) and dma	US-PGPUB; USPAT	OR	ON	2003/06/24 17:30
S71	1566	711/112	US-PGPUB; USPAT	OR	ON	2003/06/24 17:30
S72	878	710/22.ccls.	US-PGPUB; USPAT	OR	ON	2004/11/15 10:40
S73	224	710/22.ccls. and ((first and second) near2 memory)	US-PGPUB; USPAT	OR	ON	2004/11/15 10:42
S74	1	10/621997	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 10:43
S75	614	dma with refresh$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 10:43
S77	1	10/621997	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 13:20
S78	142	710/23.ccls. and (dma (direct adj memory adj access))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 13:21
S79	133	S78 and (memory with access$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 13:21
S80	121	S78 and (memory with access$3 with (cpu dma))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 13:28
S81	23	S78 and (cpu with (delay$3 suspen$4 prevent$3 wait$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 13:28
S83	59	((dma and cpu) with portion with memory) and (cpu with (delay$3 suspen$4 prevent$3 wait$3))	USPAT	OR	ON	2004/11/15 15:46
S85	1161	(dma adj2 control$4) and (cpu with (delay$3 suspen$4 prevent$3 wait$3))	USPAT	OR	ON	2004/11/15 15:47
S86	237	S85 and ((first and second) adj2 memory)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 15:47
S87	366	(dma adj2 control$4) and (cpu with (delay$3 suspen$4 prevent$3 wait$3) with memory)	USPAT	OR	ON	2004/11/15 15:47
S88	115	S87 and ((first and second) adj2 memory)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 16:21
S89	26	(suppressing with clock with cpu)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 16:22
S90	2	(suppressing with clock with cpu with delay$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 16:22

S92	1906	((stop$4 suppress$3 hold$3) with clock with cpu)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/16 10:54
S93	388	((stop$4 suppress$3 hold$3) adj5 clock adj5 cpu)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/16 11:03
S94	94	S93 and (dma near3 control$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/16 11:04
S95	17	S93 same (dma near3 control$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/16 11:47
S97	12	SUMIOKA near KAZUO	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/16 11:47
S99	120	(cpu and dma and (display adj control$4)) same (chip (integrated adj circuit) ic)	USPAT	OR	ON	2004/11/16 13:46
S10 0	64	(cpu and dma and (display adj control$4)) with (chip (integrated adj circuit) ic)	USPAT	OR	ON	2004/11/16 13:49
S10 2	47	(gps adj2 receiver) and (cpu and dma and (chip (integrated adj circuit) ic))	USPAT	OR	ON	2004/11/16 14:23
S10 6	131	(gps (global adj position$3 adj system)) and (cpu and dma and (display near2 control$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/16 14:45
S11 4	109	((first and second) adj3 (portion) adj2 (ram (random adj access adj memory)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 09:41
S11 5	8	S114 and (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 09:38
S11 6	182	((ram (random adj access adj memory)) with partition$3) and (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 09:41
S11 7	23	((ram (random adj access adj memory)) with partition$3 with portion) and (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 09:45
S11 8	26	((dram ram (random adj access adj memory)) with partition$3 with portion) and (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 09:51
S11 9	19	(single adj5 (ram dram sdram)) same (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 10:24
S12 0	34	(cpu with dma with shar$3 with (ram dram sdram))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 11:29
S12 1	8	(cpu with dma with (portion adj3 (ram dram sdram)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 11:35

S12 2	2	(cpu with dma with partition$3 with (ram dram sdram))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 11:36
S12 3	36	(cpu with dma with (partition$3 shar$3) with (ram dram sdram))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 11:39
S12 4	178	((ram dram sdram) with (separat$3 adj2 portion))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 11:39
S12 5	0	((ram dram sdram) with (separat$3 adj2 portion)) same (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 11:40
S12 6	32	((ram dram sdram) with (separat$3 adj2 portion)) and (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 11:40
S13 0	8	((cpu and dma) with (portion adj3 (ram dram sdram)))	USPAT	OR	ON	2004/11/17 12:01
S13 1	61	(ram dram sdram) with partition with (first and second)	USPAT	OR	ON	2004/11/17 13:11
S13 2	1	(ram dram sdram) with partition with ((first and second) adj2 (portion block region area))	USPAT	OR	ON	2004/11/17 12:02
S13 5	258	345/537.ccls.	US-PGPUB; USPAT	OR	ON	2004/11/17 14:14
S13 6	55	S135 and (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 14:14
S14 5	62	dma with reconfigur$6 with memory	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/16 15:16
S14 6	93	(memory with arbitrat$3 with cpu with dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/16 16:00
S14 7	48	S146 and (cpu with writ$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/16 16:08
S15 0	70	(cpu with dma) with access$3 with (concurrent$2 simultaneous$2)	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/16 16:32
S15 8	107	(cpu with dma) with (conflict ("same" adj time))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 11:02
S15 9	38	(cpu with dma) with (conflict collision collid$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 11:02
S16 0	40	(cpu with dma) with (conflict$3 collision collid$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 11:38
S16 1	1049	(cpu and dma) and ((prevent$3 detect$3) with (conflict$3 collision collid$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 11:49

S16 2	259	S161 and (dma with read$3) and (cpu with writ$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 11:41
S16 3	2	S162 and (reconfigur$6 with memory)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 11:40
S16 4	36	S161 and (dma with read$3) and (prevent$3 with (cpu with (writ$4 access$3)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 11:47
S16 5	37	S161 and (prevent$3 with (cpu with (writ$4 access$3) with memory))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 11:48
S16 6	47	(cpu and dma) with (conflict$3 collision collid$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 12:52
S16 7	127	710/308.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 12:52
S16 8	68	S167 and (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 13:03
S16 9	28	(cpu same dma) and (dma with memory with (read$3 near2 state))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 13:04
S17 3	138	(cpu with dma) with (request$3 access$3) with (simultaneous$2 concurrent$2 ("same" adj time))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 13:56
S17 4	54	(cpu with dma) with (request$3 access$3) with (simultaneous$2 concurrent$2 ("same" adj time)) with (memory dram sdram)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 13:56
S17 5	56	(cpu with dma) with (request$3 access$3) with (simultaneous$2 concurrent$2 ("same" adj time)) with (memory dram sdram storage)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 13:56
S17 7	71	(LASSERRE near SERGE).in.	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/17 15:54
S17 8	10	S177 and (@ad<"20000901")	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 14:21
S17 9	1059	(prevent$3 inhibit$3 (hold$3 adj2 off)) with writ$4 with cpu	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 15:57
S18 0	30	S179 same dma	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 15:55
S18 1	1183	(prevent$3 inhibit$3 prohibit$3 forbid$4) with writ$4 with cpu	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 15:57

S18 2	33	S181 same dma		US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/17 15:58